**CS M152A / EE M116L**

**Lab 4:**

**Scott Shi**

**Minh Le**

**1.**

**2. High Level Description**

**3. Code**

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\*.v

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\*TB.v

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\*.ucf

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**4. Synthesis Report**

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\* Advanced Synthesis Report \*

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\* Design Summary \*

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**5. Device Utilization Summary**

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| --- | --- | --- | --- |
| Slice Logic Utilization | Used | Available | Utilization |
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**6. High Level Schematics**

**7. Maximum Clock Frequency:**

**8. Waveform**